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Dated: December 22, 2004 Signature:

(Marcus J. Millet)

Docket No.: TESSERA 3.0-196 DIV (PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Smith et al.

Application No.: 10/685,361

Group Art Unit: 2812

Filed: October 14, 2003

Examiner: L. A. Gurley

For: SEMICONDUCTOR CHIP PACKAGE WITH

INTERCONNECT STRUCTURE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT

Dear Sir:

It is respectfully requested that the reference listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. A copy of the reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

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By\_

Dated: December 22, 2004

Respectfully submitted,

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Substitute for form 1449A/B/PTO				Complete if Known		
002		•		Application Number	10/685,361	
II.	<b>IFORMATION</b>	l Di	SCLOSURE	Filing Date	October 14, 2003	
S	TATEMENT B	3Y /	APPLICANT	First Named Inventor	John W. Smith	
				Art Unit	2812	
	(Use as many she	eets as	s necessary)	Examiner Name	L. A. Gurley	
Sheet	1	of	1	Attorney Docket Number	TESSERA 3.0-196 DIV	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number  Number-Kind Code <sup>2</sup> ( <i>if known</i> )	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	AA	US-6,002,168	12-14-1999	BELLAAR ET AL.		

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document  Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (il known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T⁵

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